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To: Examiner, David Nhu
Firm: USPTO
Fax No. 571 273-8300
From: William S. Frommer
Date: March 11, 2008
Re: U.S. Patent Application Serial No. 10/540,720
Our Ref.: 450101-04870.1

No. of Pages: 3
(including cover page)

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PATENT
450101-04870.1IN THE UNITED STATES PATENT AND TRADEMARK OFFICERECEIVED
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MAR 12 2008

Applicants : Koichiro KISHIMA et al. Notice of Allowance
Dated: 01/04/2008

Serial No. : 10/540,720

For : METHOD FOR MANUFACTURING SEMICONDUCTOR
SUBSTRATE AND SEMICONDUCTOR SUBSTRATE

Filed : January 13, 2006

Examiner : David Nhu

Art Unit : 2818

Confirmation No. : 2419

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Barnet Shindler
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March 12, 2008
Date of Signature

RESPONSE TO EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This is in response to the Examiner's Statement of Reasons for Allowance, which
accompanied the Notice of Allowance mailed January 4, 2008. To the extent the Examiner's

PATENT
450101-04870.1

Statement of Reasons for Allowance states, implies or is construed to mean that the claims are allowable over the prior art of record because the Examiner believes the claims should be interpreted to include one or more features or limitations not recited therein, Applicants' attorney disagrees with such an interpretation. Moreover, it is Applicants' contention that there is no particular limitation in the allowed claims that is more critical than any other. The issuance of the Examiner's Statement of Reasons for Allowance should not be construed as a surrender by Applicants of any subject matter. It is the intent of Applicants, by their attorney, to construe the allowed claims so as to cover the invention disclosed in the instant application and all equivalents to which the claimed invention is entitled.

Respectfully submitted,

FROMMER LAWRENCE & HAUG LLP
Attorneys for Applicants

By

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